

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3568696	memory or storage	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 10:49
L2	3666	1 and standby and sleep	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 11:02
L3	836	2 and sleep with (power or supply or voltage) with control\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 10:54
L4	334	3 and standby with (power or supply or voltage) with control\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 10:54
L5	222	4 and (transistor or switch\$5) with (control\$5 or gate) with (power or supply or voltage)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 11:07
L6	118	5 and (transistor or switch\$5) with (control\$5 or gate) with (power or supply or voltage) with standby	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 10:56
L7	68	6 and (transistor or switch\$5) with (control\$5 or gate) with (power or supply or voltage) with sleep	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 10:56
L8	3	7 and standby with sleep with amplif\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 11:04
L9	16901	1 and (cmos or cross adj coupl\$5 or cross-coupl\$5 or inverter) with amplif\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 11:05
L10	1	9 and (transistor or switch\$5) with (control\$5 or gate) with amplif\$5 with standby with sleep	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 11:08